

Sardar Vallabhbhai National Institute of Technology SURAT – 395 007, INDIA Sponsored Research Project, Computer Science & Engineering Department.

ADVERTISEMENT NO.: DoCSE/ISEAPIII/PA/2024-25

Date: 28th August, 2024

Applications are invited on plain paper from eligible candidates for the position of JRF/Project Associate (2 Posts) in a purely time bound research project undertaken in the Department of Computer Science & Engineering of this Institute.

2.	Name of the Position Name of the Research Project Name of the Sponsoring Agency	 : (1) Junior Research Fellow and/or (2) Project Associate : Information Security Education Awareness Project III : Ministry of Electronics & Information Technology, Govt of India, New Delhi.
4.	Consolidated Fellowship/Compensation	: Rs. 35,000/- p.m. (upto March 2024), plus contingency of Rs 20000/- per annum on a prorata basis.
5.	Qualification & Experience	 Junior Research Fellow - PhD/M.Tech./M.E. in Computer Science & Engineering/Computer Engineering/Information Technology or equivalent branches selected through a process describing through any of the following: a) Scholars who are selected through National Eligibility Test – CSIR-UGC NET and GATE b) The selection process through National Level Examination conducted by Central Govt. departments and their agencies and institutions. Project Associate – M.Tech./M.E. in Computer Science & Engineering/Computer Engineering/Information Technology or equivalent branches.
6.	Desirable	: Candidates with strong background/interest in Information

Interested eligible persons may apply in the standard format as attached herewith, giving full bio-data in the format attached with this advertisement, along with attested copies of testimonials and come along for the interview scheduled at 10.30 hrs on 13th September, 2024, in the Department of Computer Science & Engineering of the institute. **Preference will be given to the SC/ST/OBC candidates as per the Govt rules**.

Security & Machine Learning, will be preferred.

-/sd Principal Investigator, ISEA Project Phase III SVNIT, Surat